

10/075121



BEST AVAILABLE COPY

U.S. UTILITY Patent Application

PATENT NUMBER and
ISSUE DATE

| APPL NUM | FILING DATE | CLASS | SUBCLASS | GAU | EXAMINER |
|----------|-------------|-------|----------|------|------------|
| 10075121 | 02/13/2002 | 710 | 305 | 2181 | Wang PATEL |

**APPLICANTS: Bolt Thomas; Moon William; 2112

**CONTINUING DATA VERIFIED:

** FOREIGN APPLICATIONS VERIFIED:

| | | |
|---|---|----------------------------------|
| PG-PUB | DO NOT PUBLISH <input type="checkbox"/> | RESCIND <input type="checkbox"/> |
| Foreign priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no | | ATTORNEY DOCKET NO |
| 35 USC 119 conditions met <input type="checkbox"/> yes <input type="checkbox"/> no | | Q02-1031-US1 |
| Verified and Acknowledged Examiners's initials | | |
| TITLE : Use of the universal serial bus as an internal architecture within IDE disk array | | |
| U.S. DEPT. OF COMM./PAT. & TM-PTO-436L (Rev. 12-94) | | |

| NOTICE OF ALLOWANCE MAILED | | CLAIMS ALLOWED | |
|---|--|----------------------|----------------------|
| | | Total Claims | Print Claim for O.G. |
| Assistant Examiner | | DRAWING | |
| | | Sheets Drwg. | Figs. Drwg. |
| Primary Examiner | | Print Fig. | |
| PREPARED FOR ISSUE | | Application Examiner | |
| WARNING: The information disclosed herein may be restricted. Unauthorized disclosure may be prohibited by the United States Code Title 35, Sections 122, 181 and 368, Possession outside the U.S. Patent & Trademark Office is restricted to authorized employees and contractors only. | | | |

ISSUE FEE

Amount Due Date Paid

☐ TERMINAL
DISCLAIMER

FILED WITH:

☐ DISK (CRF)☐ CD-ROM
(Attached in pocket on right inside flap)